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| No. of Pages: | | Date: | August 25, 2005 |
| Re: | RESPONSE TO OFFICE ACTION DATED MAY 26, 2005 | File: | 2008.001982 |

☐ Urgent ☒ For Review ☐ Please Comment ☒ Please Reply ☒ Please Handle

* w/o coversheet

ORIGINAL: _____ Will follow ☒ Will not followU.S. Patent Application Serial No. 10/712,150
"Compression Circuit for Testing Memory Device"
Inventors: Todd A. Merritt et al.
Examiner: Joseph D. Torres
Art Unit: 2133**CONFIDENTIALITY NOTE**

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AUG 25 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
TODD A. MERRITT
NICHOLAS VANHEEL

Serial No.: 10/712,150

Filed: NOVEMBER 13, 2003

For: COMPRESSION CIRCUIT FOR
TESTING MEMORY DEVICE

Group Art Unit: 2133

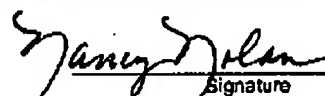
Examiner: JOSEPH D. TORRES

Conf. No.: 8235

Atty. Dkt.: 2008.001982/98-0303.01
CUSTOMER NO.: 23720

RESPONSE TO OFFICE ACTION DATED MAY 26, 2005

MAIL STOP AMENDMENT
Commissioner for Patents
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| CERTIFICATE OF TRANSMISSION 37 C.F.R. 1.8 | |
| I hereby certify that this document is being transmitted via facsimile to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, to facsimile number (571) 273-8300 on the date below: | |
| 8-25-05 Date |  Signature |

Sir:

This paper is submitted in response to the Office Action mailed May 26, 2005, for which the shortened three-month statutory response date is August 26, 2005. Since this paper is being submitted on August 25, 2005, this paper is timely filed.

If an extension of time is required to enable this paper to be timely filed and there is no separate Petition for Extension of Time filed herewith, this paper is to be construed as also constituting a Petition for Extension of Time Under 37 CFR § 1.136(a) for a period of time sufficient to enable this document to be timely filed.

No fee is believed to be due in connection with the filing of this document. However, should any fee under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to

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